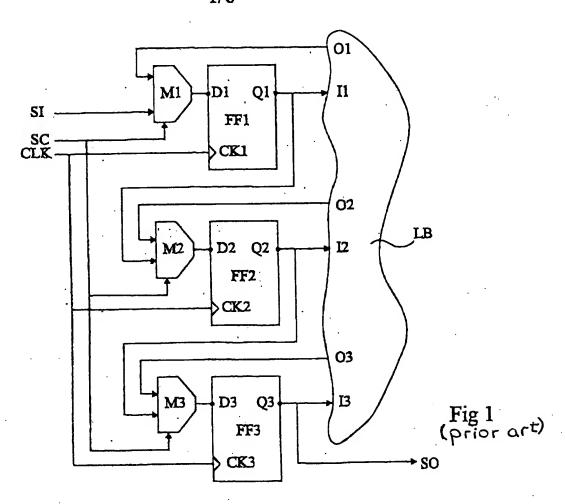
Replacement Sheet
INTEGRATED CIRCUIT TESTING METHOD AND SYSTEM
Jacques PRUNIER
Serial No.: 10/083,714
Docket No.: \$1022.80854US00

1/6



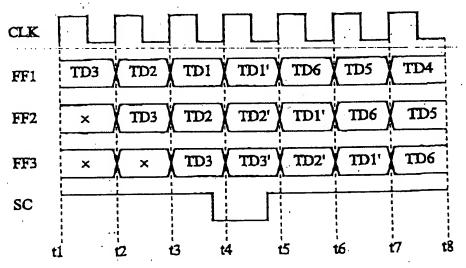


Fig 2 (prior art)

2/6

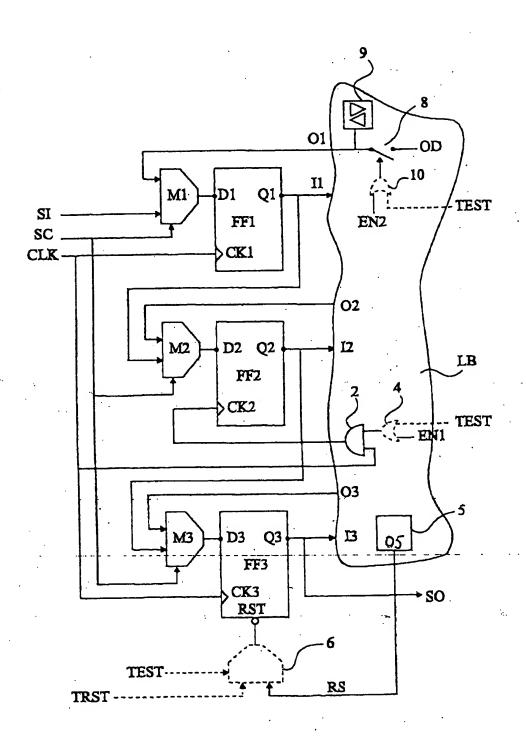


Fig 3 (prior art)

3/6

